



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Mark S. Moir et al.
 Title: OBSTRUCTION-FREE SYNCHRONIZATION FOR SHARED DATA STRUCTURES
 Application No.: 10/620,748 Filed: July 16, 2003
 Examiner: Mano Padmanabhan Group Art Unit: 2188
 Atty. Docket No.: 004-8252

September 27, 2004

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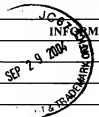
David W. O'Brien

Date

Respectfully submitted,

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U.S. Department of Commerce, Patent and Trademark Office		Attorney Docket No.: 004-8252
 <p>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</p> <p>(Use several sheets if necessary)</p>		Application No.: 10/620,748
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NON PATENT LITERATURE DOCUMENTS		
*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
	1	Afek, Yehuda et al., " <i>Long-Lived Renaming Made Adaptive</i> ", 18 th Annual ACM Symposium on Principles of Distributed Computing, pages 91-104, 1999.
	2	Afek, Yehuda, " <i>Wait-Free Made Fast</i> ", 27 th Annual ACM Symposium on Theory of Computing, pages 538-547, 1995.
	3	Agesen, Ole et al., " <i>DCAS-Based Concurrent Deques</i> ", 12 th Annual ACM Symposium on Parallel Algorithms and Architectures, pages 137-146, July 2000.
	4	Anderson, James H. et al., " <i>Using Local-Spin k-Exclusion Algorithms to Improve Wait-Free Object Implementations</i> ", 12 th Annual ACM Symposium on Principles of Distributed Computing, November 1995 (revised 1996, 1997).
	5	Arora, Nimar S. et al., " <i>Thread Scheduling for Multiprogrammed Multiprocessors</i> ", 10 th Annual ACM Symposium on Parallel Algorithms and Architectures, pages 119-129, 1998.
	6	Attiya, Hagit et al., " <i>An Adaptive Collect Algorithm with Applications</i> ", Dept. of Computing Science, The Technion, Israel, May 10, 2001.
	7	Barnes, Greg, " <i>A Method for Implementing Lock-Free Shared Data Structures</i> ", 5 th Annual ACM Symposium on Parallel Algorithms and Architectures, pages 261-270, 1993.
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	12	Greenwald, Michael B., " <i>Non-Blocking Synchronization and System Design</i> ", PhD Thesis, Stanford University Technical Report STAN-CS-TR-1624, Palo Alto, California, August 1999.
	13	Herlihy, Maurice, " <i>A Methodology for Implementing Highly Concurrent Data Objects</i> ", ACM Transactions on Programming Languages and System, pages 745-770, November 1993.
	14	Herlihy, Maurice, " <i>Dynamic-Sized Lockfree Data Structures</i> ", Sun Microsystems Technical Report SMLI TR-2002-112, June 2002.
	15	Herlihy, Maurice et al., " <i>Linearizability: A Correctness Condition for Concurrent Objects</i> ", ACM Transactions on Programming Languages and Systems, pages 463-492, July 1990.
	16	Herlihy, Maurice et al., " <i>The Repeat Offender Problem: A Mechanism for Supporting Dynamic-Sized Lock-Free Data Structures</i> ", Sun Microsystems Technical Report SMLI TR-2002-112, June 2002.
Examiner	/Sheng Jen Tsai/	Date Considered 04/29/2008
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.		

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	17	Herlihy, Maurice et al., "Transactional Memory: Architectural Support for Lock-Free Data Structures", 20 th International Symposium in Computer Architecture, 1993.
	18	Herlihy, Maurice et al., "Obstruction-Free Synchronization: Double-Ended Queues as an Example", 23 rd International Conference on Distributed Computing, May 2003.
	19	Israeli, Amos et al., "Disjoint-Access-Parallel Implementations of Strong Shared Memory Primitives", 13 th Annual ACM Symposium on Principles of Distributed Computing, pages 151-160, 1994
	20	Lampert, Leslie, "How to Make a Multiprocessor Computer that Correctly Executes Multiprocess Programs", IEEE Transactions on Computers, September 1979.
	21	Luchangco, Victor et al., "Nonblocking k-compare-single-swap", 15 th Annual ACM Symposium on Parallel Algorithms and Architectures, June 2003.
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	24	Michael, Maged M. et al., "Simple, Fast and Practical Non-Blocking and Blocking Concurrent Queue Algorithms", 15 th Annual ACM Symposium on Principles of Distributed Computing, pages 267-276, 1996.
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	26	Moir, Mark, "Laziness Pays! Using Lazy Synchronization Mechanisms to Improve Non-Blocking Constructions", 19 th Annual ACM Symposium on Principles of Distributed Computing, 2000.
	27	Moir, Mark, "Practical Implementations of Non-Blocking Synchronization Primitives", 16 th Annual ACM Symposium on Principles of Distributed Computing, 1997.
	28	Moir, Mark, "Transparent Support for Wait-Free Transactions", 11 th International Workshop on Distributed Algorithms, 1997.
	29	Moir, Mark et al., "Wait-Free Algorithms for Fast, Long-Lived Renaming", Science of Computer Programming, August 1994.
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	31	Shavit, Nir et al., "Software Transactional Memory", Distributed Computing, Special Issue (10), 1997.
	32	Trieber, R., "Systems Programming: Coping with Parallelism", IBM Technical Report RJ5118, April 23, 1986.
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